

FIG 1

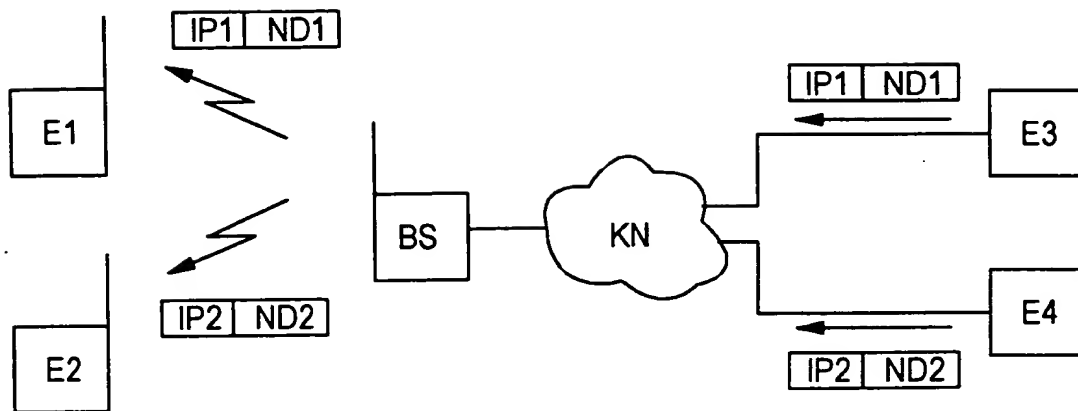
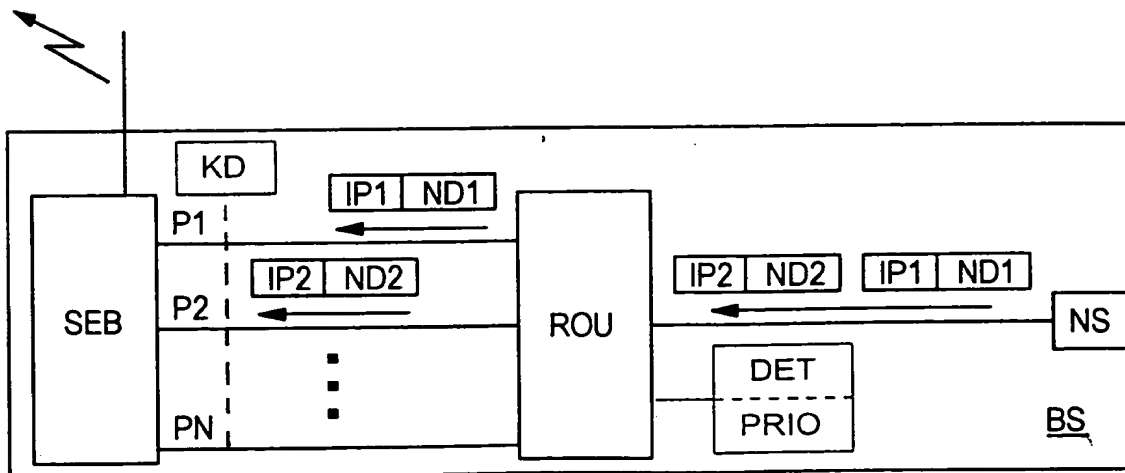


FIG 2



The diagram illustrates the E1 system architecture. A horizontal line at the top represents the power supply, with a lightning bolt symbol indicating a connection to ground. The system is enclosed in a rectangular box labeled 'E1' in the top left corner. Inside the box, the components are arranged as follows:

- SEE (Signal Error Encoder)**: Located in the top right, connected to the power supply line.
- IP1 (Input Processor 1)** and **ND1 (Noise Detector 1)**: A small rectangular block containing two sub-blocks, positioned to the right of the SEE and UM blocks.
- UM (User Module)**: A large rectangular block in the bottom right, receiving input from the SEE and the IP1/ND1 block.
- KD (Keyboard Driver)**: A large rectangular block in the bottom center, connected to the UM by a double-headed arrow labeled 'ND1'.
- SIO (Serial Input/Output)**: A large rectangular block in the bottom left, connected to the KD by a double-headed arrow labeled 'DND1'.

The overall flow of data is from the power supply to the SEE, then to the UM, which interacts with the KD and SIO via the ND1 and DND1 signals respectively. The IP1 and ND1 block also provides input to the UM.